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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/882,417  
Filed: June 15, 2001  
Inventor(s):  
Eric G. Chambers  
James R. Magro  
Dan S. Mudgett

Title: A Circuit and Method For  
Correcting Erroneous Data  
In Memory For Pipelined  
Reads

§ Examiner: Abraham, Esaw T  
§ Group/Art Unit: 2133  
§ Atty. Dkt. No: 5500-67700  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

\_\_\_\_\_  
Stephen J. Curran  
Printed Name

\_\_\_\_\_  
Signature

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6/17/04  
Date

RESPONSE TO OFFICE ACTION OF  
March 17, 2004

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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JUN 25 2004  
Technology Center 2100

Dear Sir:

This paper is submitted in response to the Office Action of March 17, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.